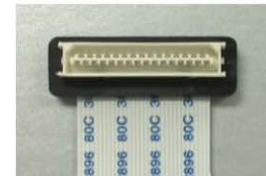
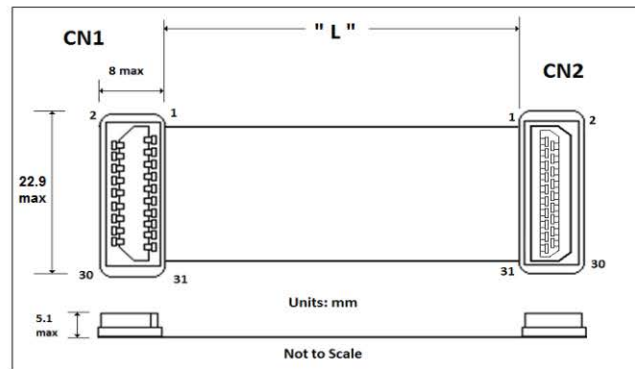


- | CN1 | CN2 |
|-----|-----|
| 1   | 1   |
| 2   | 2   |
| 3   | 3   |
| 4   | 4   |
| 5   | 5   |
| 6   | 6   |
| 7   | 7   |
| 8   | 8   |
| 9   | 9   |
| 10  | 10  |
| 11  | 11  |
| 12  | 12  |
| 13  | 13  |
| 14  | 14  |
| 15  | 15  |
| 16  | 16  |
| 17  | 17  |
| 18  | 18  |
| 19  | 19  |
| 20  | 20  |
| 21  | 21  |
| 22  | 22  |
| 23  | 23  |
| 24  | 24  |
| 25  | 25  |
| 26  | 26  |
| 27  | 27  |
| 28  | 28  |
| 29  | 29  |
| 30  | 30  |
| 31  | 31  |

**CN1 = DF9-31S-1V MOLDED TO FFC**  
**CN2 = DF9-31P-1V MOLDED TO FFC**  
**FFC INSULATION = UL2896**  
**FLAT CONDUCTOR = TIN PLATED COPPER**  
**LENGTH "L" = 145mm +/- 2.5mm**  
**100% Test for Electrical Continuity.**

**FFC DIMENSIONS:**  
**PITCH = 0.5 mm**  
**CONDUCTOR WIDTH = 0.30mm**  
**CONDUCTOR THICKNESS = 0.035mm**  
**TOTAL PITCH = 15 mm**  
**WIDTH = 16 mm**  
**MARGIN = +/- 0.15**

**RoHS Compliant**



|            |               |   |
|------------|---------------|---|
| DWG BY: PT | DATE: 8/14/14 | DRAWINGS ARE NOT TO SCALE. ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED. ALL TOLERANCES ARE +/- 0.50" UNLESS OTHERWISE SPECIFIED |
| CHK BY:    | DATE:         |   |
| APPR: -    | DATE: -       |   |

**REVISION HISTORY**

| REV | DATE    | APPROVED |
|-----|---------|----------|
| --- | 8/14/14 | PT       |
|     |         |          |
|     |         |          |
|     |         |          |



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